Ministry of Education and Science of Ukraine Dnipro university of technology

Department of Electric Drive



«APPROVED» Head of the Department of Electric Drive Khudolii S.S. __________ «30» August 2022

WORK PROGRAM OF THE ACADEMIC DISCIPLINE

«Electronics, microprocessor technology and automation tools»

Field of study	14 Electrical engineering
Specialty	141 Electrical energetics, electrical
	engineering and electromechanics
Academic level	first (bachelor)
Academic program	«Electrical energetics, electrical
	engineering and electromechanics»
Specialization	-
Status	normative
Total workload	12 credits ECTS (360 hours)
Type of summative assessment	differentiated test
Period of study	3, 4 semesters (5-8 terms)
Language of study	English

Lecturers: Assoc. Prof. Beshta O.O., Assoc. Prof. Yalanskyi O.A.

Prolonged: for: 20_/20_ a.y. ____(____) «__»_ 20_. for 20_/20_ a.y. ____(____) «__»_ 20_.

> Dnipro DNIPROTECH 2022

Work program of the academic discipline «Electronics, microprocessor technology and automation tools» for bachelors of the educational and professional program «Electrical energetics, electrical engineering and electromechanics» of the specialty 141 Electrical energetics, electrical engineering and electromechanics / Dnipro University of Technology, Department of Electric Drive. – D.: DNIPROTECH, 2022 - 17p.

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The work program regulates:

- the aim of the discipline;
- the disciplinary learning outcomes generated through the transformation of the intended learning outcomes of the degree program;
- basic disciplines;
- volume and distribution by forms of organization of the educational process and types of classes;
- discipline program (thematic plan by type of training);
- algorithm for assessing the level of achievement of disciplinary learning outcomes (scales, tools, procedures and assessment criteria);
- tools, equipment and software;
- recommended sources of information.

The work program is designed to implement a competency approach in planning an education process, delivery of the academic discipline, preparing students for control activities, controlling the implementation of educational activities, internal and external quality assurance in higher education, accreditation of degree programs within the specialty.

Approved by the decision of the Scientific and Methodological Commission of the specialty 141 Electrical energetics, electrical engineering and electromechanics (protocol №21/22-07 of 14.07.2022).

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1 AIM OF THE DISCIPLINE

In the educational and professional program «Electrical energetics, electrical engineering and electromechanics» of the specialty 141 Electrical energetics, electrical engineering and electromechanics the distribution of program learning outcomes (PLO) for the organizational forms of the educational process is done. In particular, the following learning outcomes are attributed to the discipline Φ 7 «Electronics, microprocessor technology and automation tools»:

ПР02	To know and understand the theoretical foundations of metrology and electrical
	measurements, the principles of automatic control devices, relay protection and
	automation, have the skills to perform appropriate measurements and use these
	devices to solve professional problems
ПР06	To apply application software, microcontrollers and microprocessor technology to
	solve practical problems in professional activities

The aim of the discipline is to form competences in the construction, principle of operation and analysis of processes in electronic circuits, microprocessor devices and sensors of automation systems, programming of microprocessor controllers.

The implementation of the aim requires transforming program learning outcomes into the disciplinary ones as well as an adequate selection of the contents of the discipline according to this criterion.

Code	Disciplinary learning outcomes (DLO)		
PLO			
	ПР02.01-Ф7	To understand the purpose, principle of operation and parameters of electronic elements.	
ПР02	ПР02.02-Ф7	To understand the purpose, principle of operation and parameters of analog electronic devices	
11P02	ПР02.03-Ф7	To understand the purpose, principle of operation and parameters of digital electronic devices	
	ПР02.04-Ф7	To understand the purpose, principle of operation and parameters of secondary power sources	
	ПР06.01-Ф7	To understand the architecture, structure, functionality of the basic microcontroller model, develop algorithms, compile and debug programs. Understand the principles of information display systems.	
ПР06	$\Pi P06.02-\Phi7$ Develop basic electrical schemes for connecting external devices and organize data exchange.		
11100	ПР06.03-Ф7	Choose a controller and expansion modules to solve the task of automating a local industrial plant.	
	ПР06.04-Ф7	Choose the type and model of the sensor, based on the task and conditions of detection, properties of the object and parameters of the environment, taking into account economic feasibility.	

2 INTENDED DISCIPLINARY LEARNING OUTCOMES

3 BASIC DISCIPLINES

Subjects Achieved learning outcomes	
Б1 Higher mathematics,	IIP08 To select and apply suitable methods for analysis
52 General Physics,	and synthesis of electromechanical and electrical systems
-	with specified parameters.

Subjects	Achieved learning outcomes	
	ΠΡ07 To carry out analysis of processes in electrical, electrical and electromechanical equipment, relevant complexes and systems.	
Б5 Theoretical foundations of electrical engineering	IIP05 To know the basics of the theory of the electromagnetic field, methods of calculating electric circuits and be able to use them to solve practical problems in professional activities.	

4 WORKLOAD DISTRIBUTION BY THE FORM OF EDUCATIONAL PROCESS ORGANIZATION AND TYPES OF CLASSES

	oa 's	Distribution by forms of education, hours					
Type of	Workloa d hours	Full-time		Part-time		Extra	amural
classes	Vo d h	Classes	Individual	Classes	Individual	Classes	Individual
	1	(C)	work (IW)	(C)	work (IW)	(C)	work (IW)
lectures	180	67	113	-	-	16	164
practical				-	-	-	-
laboratory	180	75	105	-	-	16	164
seminars	-	_	_	-	_	-	_
TOTAL	360	142	218	_	_	32	328

5 DISCIPLINE PROGRAM BY TYPES	OF CLASSES
5 DISCH LINE I NOURANI DI TITES	OF CLADDLD

Code DLO	Types and topics of classes	Volume of components, <i>hours</i>
	3 semester (5-6 terms)	
	LECTURES	90
ПР02.01-Ф7	1. Element base of electronics.	20
	1.1. Passive components of electronic circuits.	
	1.2. Electrophysical properties of semiconductors.	
	1.3. Classification and general characteristics of semiconductor	
	devices.	
	1.4. Diode group devices.	
	1.5. Bipolar and field-effect transistors.	
	1.6. Thyristors.	
	1.7. IGBT transistors.	
	1.8. Elements of optoelectronics	
ПР02.02-Ф7	2. Analog electronic devices	20
	2.1. Parameters and characteristics of analog devices.	
	2.2. Electronic amplifiers of electrical signals.	
	2.3. Operational amplifiers, analog circuits for operational	
	amplifiers.	
	2.4. Generators of sinusoidal oscillations.	
ПР02.03-Ф7	3. Digital electronic devices.	20
	3.1. Fundamentals of logic algebra and digital integrated	
	circuits of logic elements	
	3.2. Sequential circuits: triggers, counters, registers.	
	3.3. Combination devices: encoders, decoders, adders,	
	multiplexers. comparison schemes	
	3.4. Digital-to-analog and analog-to-digital converters (DAC	
	and ADC).	

4. Secondary power sources.	30
4.1. Uncontrolled rectifiers. Parameters and characteristics.	20
LABORATORY WORKS	90
Study of structures, markings and images of radio components	6
Investigation of semiconductor diodes and zener diodes	6
Investigation of a bipolar transistor	6
Thyristor research	6
Calculation and study of low frequency amplifier on bipolar transistor	6
Calculation of amplifiers on the operational amplifier: inverting, non-inverting and sub-tracting	6
Investigation of of amplifiers on the operational amplifier: inverting, non-inverting and sub-tracting	6
Investigation of the generator of sinusoidal oscillations on the operational amplifier	6
Investigation of passive forming devices	6
Investigation of relaxation generators on the operational amplifier	6
Investigation of the basic element of transistor-transistor logic	6
Investigation of trigger circuits	6
	4
	8
-	6
Total for 3 semester:	180
4 semester (7 term)	
LECTURES	45
1. Components of microprocessor devices. Number	5
systems. General infor-mation about microprocessor	
gratema Implementation of emithmatic calcula tions and	
systems. Implementation of arithmetic calcula-tions and	
performance of logical calculations	
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission,	
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.	
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission,	
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.	
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.	
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.	20
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.1.4. Implementation of arithmetic and logical operations.2. Single-chip microcontroller and single-chip microcontroller (OMC) com-mand system of the MCS-51	20
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.1.4. Implementation of arithmetic and logical operations.2. Single-chip microcontroller and single-chip microcontroller (OMC) com-mand system of the MCS-51 family	20
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.1.4. Implementation of arithmetic and logical operations.2. Single-chip microcontroller and single-chip microcontroller (OMC) com-mand system of the MCS-51 family2.1. MCS-51 microcontroller architecture. Memory organization	20
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.1.4. Implementation of arithmetic and logical operations.2. Single-chip microcontroller and single-chip microcontroller (OMC) com-mand system of the MCS-51 family2.1. MCS-51 microcontroller architecture. Memory organization and registers.	20
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.1.4. Implementation of arithmetic and logical operations.2. Single-chip microcontroller and single-chip microcontroller (OMC) com-mand system of the MCS-51 family2.1. MCS-51 microcontroller architecture. Memory organization and registers.2.2. MCS-51 microcontroller instruction system.	20
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.1.4. Implementation of arithmetic and logical operations.2. Single-chip microcontroller and single-chip microcontroller (OMC) com-mand system of the MCS-51 family2.1. MCS-51 microcontroller architecture. Memory organization and registers.2.2. MCS-51 microcontroller instruction system.2.3. Groups and types of instructions.	20
performance of logical calculations1.1. The purpose of the discipline. Methods of transmission, storage and processing of informatio.1.2. Classification and purpose of microprocessor devices.1.3. Number systems. Positional number systems.1.4. Implementation of arithmetic and logical operations.2. Single-chip microcontroller and single-chip microcontroller (OMC) com-mand system of the MCS-51 family2.1. MCS-51 microcontroller architecture. Memory organization and registers.2.2. MCS-51 microcontroller instruction system.	20
	4.2.Schemes of single-phase and three-phase rectifiers.4.3.Smoothing filters4.4.Voltage and current stabilizers.LABORATORY WORKSStudy of structures, markings and images of radio componentsInvestigation of semiconductor diodes and zener diodesInvestigation of a bipolar transistorThyristor researchCalculation and study of low frequency amplifier on bipolar transistorCalculation of amplifiers on the operational amplifier: inverting, non-inverting and sub-tractingInvestigation of of amplifiers on the operational amplifier: inverting, non-inverting and sub-tractingInvestigation of the generator of sinusoidal oscillations on the operational amplifierInvestigation of relaxation generators on the operational amplifierInvestigation of trigger circuitsInvestigation of trigger circuitsInvestigation of DAC and ADCInvestigation of uncontrolled rectifier with filters LECTURES 1.Components of microprocessor devices. Number

	3.1. Classification of typical program constructs.	
	3.2. Program implementation of arithmetic data processing and logical operations.	
	3.3. Implementation of basic program constructs. Branching (condition), cycles.	
	3.4. Bit processor. Discrete data processing.	
	3.5. Debugging programs.	
ΠΡ06.01.Φ7		10
ПР06.01-Ф7, ПР06.02-Ф7	family microcontrollers	10
	4.1. Parallel ports of the microcontroller. Purpose, modes of operation	
	4.2. Circuitry of parallel ports.	
	4.3. External devices connection diagrams.	
	4.4. Parallel, discrete and serial communication via ports.	
	4.5. Timers / counters. Purpose, modes of operation.	
	4.6. Organization of time intervals. Generating signals with specified time parame-ters.	
	4.7. Interrupt system. Purposes and modes.	
	4.8. Processing of external interrupts and interrupts from built-in devices.	
	4.9. Universal asynchronous receiver / transmitter (UART).	
	Purpose, circuitry, modes of operation.	
	4.10. Sequential data exchange. Point-to-point and bus	
	networks.	45
TTD06.01.07	LABORATORY WORKS	<u>45</u> 5
ПР06.01-Ф7	The architecture of the microcontroller 8051AH family MCS-51. Full-screen FD51 debugger-simulator.	
ПР06.01-Ф7	MCS-51 family microcontroller instruction system. Typical program constructs.	8
ПР06.01-Ф7	Bit processor of the MCS-51 microcontroller. Programming in the integrated development environment IDE Keil uVision	8
ПР06.01-Ф7	Programming of arithmetic operations using microcontrollers of the MCS-51 family.	8
ПР06.01-Ф7 ПР06.02-Ф7	MCS-51 assembler. Training and adjusting stand EV8031 / AVR	8
ПР06.01-Ф7 ПР06.02-Ф7	Programming of data transmission in parallel format. Methods of displaying information.	8
	Total for 7 term:	90
	4 semester (8 term)	. 7
	LECTURES	45
ПР06.03-Ф7	1. Automation, its purpose. Classification of automated	6
	control systems and technical means of automation.	-
	Programmable logic controllers (PLCs). PLC programming	
	languages	
	1.1. The purpose of automation of technological processes.	
	1.2. Classification of automatic and automated systems. Local	
	automation systems.	

	1.3. Programmable logic controllers (PLCs). PLC programming	
HD0602 #7	languages.	0
ПР06.03-Ф7	2. PLC Zelio Logic: overview, technical capabilities,	8
	hardware configurations. Ladder Diagram programming	
	language	
	2.1. PLC Zelio Logic: appointment technical capabilities.	
	2.2. Operating modes.	
	2.3. Zelio Analog interfaces.	
	2.4. Zelio Soft programming environment.	
	2.5. Ladder Diagram (LD) programming language.	
	2.6. Programming of typical tasks and logical functions	
	2.7. Debugging programs.	
ПР06.03-Ф7	3. PLC Zelio Logic functions (internal program functional	8
	blocks)	
	3.1. Assignment of functions (internal function blocks).	
	3.2. Main functions: timers, counters, comparators.	
	3.3. Implementation of logic functions using ladder diagrams	
	3.4. Debugging programs.	
TTD06.02		12
ПР06.03-Ф7	4. Functional blocks programming using special functions (SFC)	13
	4.1. Programming in the language of functional blocks	
	(Functional Block Diagram, FBD)	
	4.2. Basic functions: timers, counters, comparators, cam	
	controller.	
	4.3. Additional and service functional blocks.	
	4.4. Logical elements.	
	4.5. Grafcet special blocks (Special Functions, SFC).	
	4.6. Macros	
	4.7. Supervisor window.	
	▲	
ПР06.04-Ф7	4.8. Debugging programs.5. Sensors of automation systems	10
ΠΡ06.04-Ψ/		10
	5.1 Purpose, classification, types of industrial sensors.	
	Principles of action.	
	5.2 Photoelectric sensors.	
	5.3 Inductive sensors.	
	5.4 Ultrasonic sensors.	
	5.5 Capacitive sensors.	
	5.6 Additional equipment.	
	5.7 Circuitry and connection of sensors.	
	5.8 Summary conclusions and recommendations for selection.	
	LABORATORY WORKS	45
ПР06.03-Ф7	1.ZelioSoft 2 software shell: purpose, interface, methods of work	9
ПР06.03-Ф7	2.Implementation of logical functions in the language of Ladder	4
	diagrams.	•
ПР06.03-Ф7	3.Internal functions of Zelio modules: purpose, modes,	6
	programming methods.	-

ПР06.03-Ф7	4.Programming of a typical cyclic mechanism control task (LD	4
	language).	
ПР06.03-Ф7	5.Programming a typical cyclic mechanism control task (FBD	6
	language).	
ПР06.03-Ф7	6.Programming a typical control task of a cyclic mechanism	4
	(Grafcet language).	
ПР06.04-Ф7	7.Research of photoelectric sensors.	4
ПР06.04-Ф7	8.Research of inductive sensors.	4
ПР06.04-Ф7	9.Research of ultrasonic and capacitive sensors.	4
	Total for 8 term	90
	Total for 4 semester	180
	TOTAL	360

For the implementation of the hybrid form of teaching students, the electronic resource of the e-learning in the discipline is used at the following address: https://do.nmu.org.ua/course/view.php?id=3412

6 KNOWLEDGE PROGRESS TESTING

Certification of student achievement is accomplished through transparent procedures based on objective criteria in accordance with the University Regulations "On Evaluation of Higher Education Applicants' Learning Outcomes".

The level of competencies achieved in relation to the expectations, identified during the control activities, reflects the real result of the student's study of the discipline.

6.1 Grading scales

Assessment of academic achievement of students of the Dnipro University of Technology is carried out based on a rating (100-point) and institutional grading scales. The latter is necessary (in the official absence of a national scale) to convert (transfer) grades for mobile students.

Rating	Institutional
90 100	відмінно / Excellent
74 89	добре / Good
60 73	задовільно / Satisfactory
0 59	незадовільно / Fail

The scales of assessment of learning outcomes of the DNIPROTECH students

Discipline credits are scored if the student has a final grade of at least 60 points. A lower grade is considered to be an academic debt that is subject to liquidation in accordance with the Regulations on the Organization of the Educational Process of DNIPROTECH.

6.2 Tools and procedures

The content of diagnostic tools is aimed at controlling the level of knowledge, proficiency/skills, communication, autonomy, and responsibility of the student according to the requirements of the National Qualifications Framework (NQF) up to the 6th qualification level during the demonstration of the learning outcomes

regulated by the work program.

During the control activities, the student should perform tasks focused solely on the demonstration of disciplinary learning outcomes (Section 2).

Diagnostic tools provided to students at the control activities in the form of tasks for the formative and summative knowledge progress testing are formed by specifying the initial data and a way of demonstrating disciplinary learning outcomes.

Diagnostic tools (control tasks) for the formative and summative knowledge progress testing are approved by the department.

Types of diagnostic tools and procedures for evaluating the formative and summative knowledge progress testing are given below.

FORMATIVE ASSESSMENT			SUMMATIVE ASSESSMENT	
training sessions	diagnostic tools	procedures	diagnostic tools	procedures
lectures	control tasks for	performing the task		determination of the
	each topic	during lectures		weighted average result of
laboratory	control tasks for	performing tasks		formative assessments;
	each laboratory	during practical		
	work	classes	complex	performing CCW during
			control work	the differentiated test at the
			(CCW)	request of the student

Diagnostic and assessment procedures

During the current control, lectures are evaluated by determining the quality of control specific tasks. Laboratory classes are assessed by the quality of the control task.

If the content of a certain type of classes is subordinated to several components of the description of the qualification level according to the NQF, the integral value of the grade can be determined taking into account the weighting coefficients set by the lecturer.

Provided that the level of results of the formative assessments of all types of training at least 60 points, the summative assessment can be carried out without the student's immediate participation by determining the weighted average value of the obtained grades.

Regardless of the results of the formative assessments, every student during the summative knowledge progress testing has the right to perform the CCW, which contains tasks covering key disciplinary learning outcomes.

The number of specific tasks of the CCW should be consistent with the allotted time for completion. The number of CCW options should ensure that the task is individualized.

The value of the mark for the implementation of the CCW is determined by the average evaluation of the components (specific tasks) and is final.

The integral value of the assessment of the implementation of the CCW can be determined taking into account the weighting coefficients established by the department for each component of the description of the qualification level of the NQF.

6.3 Criteria

Actual student learning outcomes are identified and measured relative to what is expected during the control activities using criteria that describe the student's actions to demonstrate the achievement of learning outcomes.

To assess the performance of control tasks during the formative assessment on lectures and laboratory classes the coefficient of mastery is used as a criterion, which automatically adapts the assessment indicator to the rating scale:

$$O_i = 100 \ a/m,$$

where a is a number of correct answers or significant operations performed in accordance with the solution standard; m is the total number of questions or significant operations of the standard.

Individual tasks and complex control works are assessed expertly using criteria that characterize the ratio of requirements to the level of competencies and indicators of assessment on a rating scale.

The content of the criteria is based on the competency characteristics defined by the NQF for the bachelor's level of higher education (given below).

for the 6 th qualification level of NQF (bachelor)Description ofRequirements for knowledge, proficiency/skills,Indicator			
qualification level	communication, autonomy and responsibility	evaluation	
Knowleges			
Conceptual scientific and practical knowledge, critical understanding of theories, principles, methods and concepts in the field of professional	The answer is excellent - correct, reasonable, meaningful. Characterizes the presence of: - conceptual knowledge; - high degree of knowledge of the state of the art; - critical understanding of the basic theories, principles, methods and concepts in education and professional activity	95-100	
activity and / or	The answer contains minor errors or omissions	90-94	
training	The answer is correct, but has some inaccuracies	85-89	
	The answer is correct, but has some inaccuracies and is insufficiently substantiated	80-84	
	The answer is correct, but has some inaccuracies, insufficiently substantiated and meaningful	74-79	
	The answer is fragmentary	70-73	
	The answer shows the student's vague ideas about the object of study	65-69	
	The level of knowledge is minimally satisfactory	60-64	
	The level of knowledge is unsatisfactory	<60	
	Proficiency/Skills		

General criteria for achieving learning outcomes for the 6th auglification level of NOF (bachelor)

Requirements for knowledge, proficiency/skills,	Indicator
communication, autonomy and responsibility	evaluation
	95-100
- identify problems;	
- formulate hypotheses;	
- solve problems;	
- choose appropriate methods and tools;	
- collect and interpret information logically and	
clearly;	
- use innovative approaches to solving problems	
	90-94
	85-89
-	
	80-84
-	
	74-79
	70-73
	1010
-	
	65-69
	00 07
	60-64
	00 01
	<60
	95-100
• • • • • •	
•	
- concise.	
· · ·	
-	
	90-94
flaws.	
	1
	<pre>communication, autonomy and responsibility The answer characterizes the ability to:</pre>

Description of qualification level	Requirements for knowledge, proficiency/skills, communication, autonomy and responsibility	Indicator evaluation
	Relevant communication strategy with minor flaws.	
	Good knowledge of industry issues.	85-89
	Good clarity of the answer (report) and appropriate	
	communication strategy (three requirements in total are	
	not realized)	
	Good knowledge of industry issues.	80-84
	Good clarity of the answer (report) and appropriate	
	communication strategy (four requirements not	
	implemented in total)	
	Good knowledge of industry issues.	74-79
	Good clarity of the answer (report) and appropriate	
	communication strategy (five requirements not	
	implemented in total)	
	Satisfactory knowledge of industry issues.	70-73
	Satisfactory clarity of the answer (report) and	
	appropriate communication strategy (a total of seven	
	requirements have not been implemented)	
	Partial knowledge of industry issues.	65-69
	Satisfactory clarity of the answer (report) and	
	communication strategy with errors (a total of nine	
	requirements are not implemented)	
	Partial knowledge of industry issues.	60-64
	Satisfactory clarity of the answer (report) and	
	communication strategy with errors (a total of 10	
	requirements are not implemented)	
	The level of communication is unsatisfactory	<60
	Autonomy and responsibility	100
• managing complex	Excellent command of personal management	95-100
technical or	competencies focused on:	
professional activities	1) management of complex projects, which involves:	
or projects	- research nature of educational activities, marked by the	
• ability to take	ability to independently assess various life situations,	
responsibility for	phenomena, facts, identify and defend a personal	
making and making	position;	
decisions in	- ability to work in a team;	
unpredictable work	- control of own actions;	
and / or learning	2) responsibility for decision-making in unpredictable	
contexts	conditions, including:	
• formation of	- justification of own decisions by the provisions of the	
judgments that take	regulatory framework of the industry and state levels;	
into account social,	- independence in the performance of tasks;	
scientific and ethical	- initiative in discussing problems;	
aspects	- responsibility for relationships;	
• organization and	3) responsibility for the professional development of	
management of	individuals and/or groups of individuals, which involves	
professional	- use of professionally oriented skills;	
development of	- use of evidence with independent and correct	
individuals and	argumentation;	
groups	- mastery of all types of learning activities;	

Description of qualification level	Requirements for knowledge, proficiency/skills, communication, autonomy and responsibility	Indicator evaluation
 ability to continue studies with a significant degree of autonomy 	 4) the ability to continue learning with a high level of autonomy, which includes the degree of mastery of fundamental knowledge; independence of evaluative judgments; 	
	 - a high level of general learning skills; - independent search and analysis of information sources 	
	Good mastery of personality management competencies (two requirements not met)	90-94
	Good mastery of personality management competencies (three requirements not met)	85-89
	Good mastery of personality management competencies (four requirements not met)	80-84
	Good mastery of personality management competencies (six requirements not met)	74-79
	Satisfactory mastery of personality management competencies (seven requirements not met)	70-73
	Satisfactory mastery of personality management competencies (eight requirements not met)	65-69
	The level of responsibility and autonomy is fragmentary	60-64
	The level of autonomy and responsibility is unsatisfactory	<60

7 TOOLS, EQUIPMENT AND SOFTWARE

Technical means of training.

Remote platform MOODLE, MS Teams.

Specialized software – Multisim 11.0 software package, ZelioSoft 2 software package, FD51, integrated develolopment environment Keil uVision.

8 RECOMMENDED SOURCES OF INFORMATION

Basic:

- 1. Основи електроніки : навч. посіб. / А. С. Васюра, Г. Д. Дорощенков, В. П. Кожем'яко, Г. Л. Лисенко. Вінниця : ВНТУ, 2018. 197 с.
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